Guna Sai Kiran Nekkanti

Rajamahendravaram, Andhra Pradesh
Bachelor of Technology
Major in Electronics and communication engineering
Indian Institute of Information Technology Nagpur

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CAREER OBJECTIVE

To excel in the field of electronics and to work in an innovative and competitive world which will help me to realize my potential, enhance my skills and help the organization grow.

Education

Degree/Certificate	Institute/Board	CGPA/Percentage	Year
BTech(ECE)	Indian Institute of Information Technology, Nagpur	7.79	2020-Present
Intermediate	Tirumala Junior College	9.8	2018-2020
Secondary	Bhashyam High school	9.8	2018

Coursework

•Digital Design

•HDL

•CMOS Design

•Analog Circuits

•Computer Architecture And Organization

•Microprocessors And Micro-controllers

Areas of Interest

•VLSI and FPGAs

•Physical Verfication and Design

•Digital Integrated Circuit Design

•SOC Integration

•CMOS Design and Computer Architecture

•Analog Design

Technical Skills

- **Programming**: Verilog, Python, C/C++, Java,SQL,OOPS
- **Tools**: Xilinx-Vivado, Microwind, Ngspice, Keil, Matlab, Proteus, LaTeX, Microsoft Office, VS-Code, Git, Shell
- **Operating Systems**: Windows, Linux
- **Development Boards**: Raspberry pi, Arduino
- **Communication**: English(Full Professional Proficiency), Telugu(Native or Bilingual Proficiency)

Projects

- **ALL IN ONE COUNTER** *Jan 2021 - May 2021*
  HDL Project/IIIT Nagpur Github
  – This design contains eight types of Counters in a single chip.
  – Xilinx Vivado Software is used to code and debug.
- **SPEED CONTROLLER** *Dec. 2021 - Jan. 2022*
  Analog Communication Project/IIIT Nagpur GIthub
  – This project is designed to control the speed of a DC motor using 555 IC.
  – Proteus, Simulink , Tinkercad are used to simulate.
- **BCD TO SEVEN SEGMENT DECODER** *Jan 2023 - May. 2023*
  Cmos Design Project/IIIT Nagpur Github
  – Design and Analysis of BCD TO SEVEN SEGMENT DECODER circuit in various technologies and comparing their power capacities.
  – Microwind is used to design a layout and ngspice is used to code and debug.
- **BROADSIDE ANTENNA** *Dec2022 - Jan. 2023*
  Wave Guides and antenna Project/IIIT Nagpur Github
  – Design and simulation of Broadside Array Antenna resonating at 2GHz.
  – Cst Studio is used to design.

Achievements

- **Gate**,EC Paper - Qualified *2023*

Positions of Responsibility

- **Social media Manager**, Ace-Ecell, IIIT Nagpur *Sep 2021 – Dec2022*
- **Content Writer**, Probe IIIT Nagpur *Jun 2021 – Dec2022*

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